

## Device Identity

UserDeviceName	52E3 - Polpaico			
<b><u>Overcurrent Settings</u></b>	<b><u>Normal</u></b>	<b><u>Alternate 1</u></b>	<b><u>Alternate 2</u></b>	<b><u>Alternate 3</u></b>
<b>Phase:</b>				
PhsTripBlk	Unblocked	Unblocked	Unblocked	Unblocked
FastTripBlock	Unblocked	Unblocked	Unblocked	Unblocked
TCCPMinTrip	170	100	100	100
TCC1PCurve	IEC INV (200)	104	104	104
TCC1PMultEnable	Enable	Disable	Disable	Disable
TCC1PMult	1	1	1	1
TCC1PAddEnable	Disable	Disable	Disable	Disable
TCC1PAdd	0	0	0	0
TCC1PMRTAEnable	Disable	Disable	Disable	Disable
TCC1PMRTA	0.013	0.013	0.013	0.013
TCC1PHCTEnable	Enable	Disable	Disable	Disable
TCC1PHCT Mul	5.28	32	32	32
TCC1PHCTDly	0.05	0.016	0.016	0.016
TCC2PCurve	117	117	117	117
TCC2PMultEnable	Disable	Disable	Disable	Disable
TCC2PMult	1	1	1	1
TCC2PAddEnable	Disable	Disable	Disable	Disable
TCC2PAdd	0	0	0	0
TCC2PMRTAEnable	Disable	Disable	Disable	Disable
TCC2PMRTA	0.013	0.013	0.013	0.013
TCC2PHCTEnable	Disable	Disable	Disable	Disable
TCC2PHCT Mul	32	32	32	32
TCC2PHCTDly	0.016	0.016	0.016	0.016

Ground:				
GndTripBk	Unblocked	Unblocked	Unblocked	Unblocked
FastTripBlock	Unblocked	Unblocked	Unblocked	Unblocked
TCCGMinTrip	50	50	50	50
TCC1GCurve	162	106	106	106
TCC1GMultEnable	Enable	Disable	Disable	Disable
TCC1GMult	1	1	1	1
TCC1GAddEnable	Disable	Disable	Disable	Disable
TCC1GAdd	0	0	0	0
TCC1GMRTAEnable	Disable	Disable	Disable	Disable
TCC1GMRTA	0.013	0.013	0.013	0.013
TCC1GHCTEnable	Enable	Disable	Disable	Disable
TCC1GHCT Mul	4	32	32	32
TCC1GHCTDly	0.05	0.016	0.016	0.016
TCC2GCurve	135	135	135	135
TCC2GMultEnable	Disable	Disable	Disable	Disable
TCC2GMult	1	1	1	1
TCC2GAddEnable	Disable	Disable	Disable	Disable
TCC2GAdd	0	0	0	0
TCC2GMRTAEnable	Disable	Disable	Disable	Disable
TCC2GMRTA	0.013	0.013	0.013	0.013
TCC2GHCTEnable	Disable	Disable	Disable	Disable
TCC2GHCT Mul	32	32	32	32
TCC2GHCTDly	0.016	0.016	0.016	0.016

Negative Sequence:				
NegSeqTripBlk	Blocked	Blocked	Blocked	Blocked
FastTripBlock	Unblocked	Unblocked	Unblocked	Unblocked
TCCQMinTrip	100	100	100	100
TCC1QCurve	104	104	104	104
TCC1QMultEnable	Disable	Disable	Disable	Disable
TCC1QMult	1	1	1	1
TCC1QAddEnable	Disable	Disable	Disable	Disable
TCC1QAdd	0	0	0	0
TCC1QMRTAEnable	Disable	Disable	Disable	Disable
TCC1QMRTA	0.013	0.013	0.013	0.013
TCC1QHCTEnable	Disable	Disable	Disable	Disable
TCC1QHCT Mul	32	32	32	32
TCC1QHCTDly	0.016	0.016	0.016	0.016
TCC2QCurve	117	117	117	117
TCC2QMultEnable	Disable	Disable	Disable	Disable
TCC2QMult	1	1	1	1
TCC2QAddEnable	Disable	Disable	Disable	Disable
TCC2QAdd	0	0	0	0
TCC2QMRTAEnable	Disable	Disable	Disable	Disable
TCC2QMRTA	0.013	0.013	0.013	0.013
TCC2QHCTEnable	Disable	Disable	Disable	Disable
TCC2QHCT Mul	32	32	32	32
TCC2QHCTDly	0.016	0.016	0.016	0.016



<u>Operations Sequence</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
Operations To LO	1	4	4	4
<b>Phase/Neg Sequence:</b>				
PQOper#1	TCC1	TCC1	TCC1	TCC1
PQOper#2	TCC1	TCC1	TCC1	TCC1
PQOper#3	TCC1	TCC2	TCC2	TCC2
PQOper#4	TCC1	TCC2	TCC2	TCC2
<b>Ground:</b>				
GndOper#1	TCC1	TCC1	TCC1	TCC1
GndOper#2	TCC1	TCC1	TCC1	TCC1
GndOper#3	TCC1	TCC2	TCC2	TCC2
GndOper#4	TCC1	TCC2	TCC2	TCC2

<u>Reclose Intervals</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
<b>Phase/Neg Sequence:</b>				
PQOpenInt#1	2	2	2	2
PQOpenInt#2	2	2	2	2
PQOpenInt#3	5	5	5	5
<b>Ground:</b>				
GndOpenInt#1	2	2	2	2
GndOpenInt#2	2	2	2	2
GndOpenInt#3	5	5	5	5
ResetTime	30	30	30	30

<u>Cold Load Pickup</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
CLPUBlock	Yes	Yes	Yes	Yes
<b>Phase:</b>				
CLPUPMinTrip	200	200	200	200
CLPUPCurve	117	117	117	117
CLPUPMultEnable	Disable	Disable	Disable	Disable
CLPUPMult	1	1	1	1
CLPUPAddEnable	Disable	Disable	Disable	Disable
CLPUPAdd	0	0	0	0
CLPUPMRTAEnable	Disable	Disable	Disable	Disable
CLPUPMRTA	0.013	0.013	0.013	0.013
CLPUPHCTEnable	Disable	Disable	Disable	Disable
CLPUPHCT Mul	32	32	32	32
CLPUPHCTDly	0.016	0.016	0.016	0.016
<b>Ground:</b>				
CLPUGMinTrip	100	100	100	100
CLPUGCurve	135	135	135	135
CLPUGMultEnable	Disable	Disable	Disable	Disable
CLPUGMult	1	1	1	1
CLPUGAddEnable	Disable	Disable	Disable	Disable
CLPUGAdd	0	0	0	0
CLPUGMRTAEnable	Disable	Disable	Disable	Disable
CLPUGMRTA	0.013	0.013	0.013	0.013
CLPUGHCTEnable	Disable	Disable	Disable	Disable
CLPUGHCT Mul	32	32	32	32
CLPUGHCTDly	0.016	0.016	0.016	0.016
<b>Negative Sequence:</b>				
CLPUQMinTrip	100	100	100	100
CLPUQCurve	101	101	101	101
CLPUQMultEnable	Disable	Disable	Disable	Disable
CLPUQMult	1	1	1	1
CLPUQAddEnable	Disable	Disable	Disable	Disable
CLPUQAdd	0	0	0	0
CLPUQMRTAEnable	Disable	Disable	Disable	Disable
CLPUQMRTA	0.013	0.013	0.013	0.013
CLPUQHCTEnable	Disable	Disable	Disable	Disable
CLPUQHCT Mul	32	32	32	32
CLPUQHCTDly	0.016	0.016	0.016	0.016

<u>Frequency</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
<b>Underfrequency:</b>				
UFreqEnable	Off	Off	Off	Off
UFreq1PU	56	56	56	56
UFreq1Time	100	100	100	100
UFreq2PU	56	56	56	56
UFreq2Time	100	100	100	100
<b>Overfrequency:</b>				
OFreqEnable	Off	Off	Off	Off
OFreq1PU	64	64	64	64
OFreq1Time	100	100	100	100
OFreq2PU	64	64	64	64
OFreq2Time	100	100	100	100
<b>U/OF Tripping Supervision:</b>				
Freq:MinVolt	3.6	3.6	3.6	3.6
<b>UF Loadshed Restore:</b>				
FreqRestoreEnable	Off	Off	Off	Off
Freq:81OR:PU	60.05	60.05	60.05	60.05
Freq:62Schedule	300	300	300	300
Freq:62Abort	600	600	600	600
Freq:62Transient	0.3	0.3	0.3	0.3
VoltFreqRestSupEn	Off	Off	Off	Off

**RecsTime and Control**

	<b><u>Normal</u></b>	<b><u>Alternate 1</u></b>	<b><u>Alternate 2</u></b>	<b><u>Alternate 3</u></b>
79ResetTarEnable	Enable	Enable	Enable	Enable
79ResetTar	2	2	2	2
79SeqCoorEnable	Disable	Enable	Enable	Enable
79SeqCoorOps	1	2	2	2

**Reclose Retry**

	<b><u>Normal</u></b>	<b><u>Alternate 1</u></b>	<b><u>Alternate 2</u></b>	<b><u>Alternate 3</u></b>
RecloseRetryEnable	Disable	Disable	Disable	Disable
RecloseRetryAttempts	1	1	1	1
RecloseRetryInterval	60	60	60	60

**Voltage**

	<b><u>Normal</u></b>	<b><u>Alternate 1</u></b>	<b><u>Alternate 2</u></b>	<b><u>Alternate 3</u></b>
<b>Undervoltage:</b>				
UVolt1PEnable	Off	Off	Off	Off
UVolt3PEnable	Off	Off	Off	Off
UVolt1P/3Pinhibit	Off	Off	Off	Off
UVolt1PPU	11.52	11.52	11.52	11.52
UVolt1PTime	100	100	100	100
UVolt3PPU	11.52	11.52	11.52	11.52
UVolt3PTime	100	100	100	100
<b>Overvoltage:</b>				
OVoltEnable	Off	Off	Off	Off
OVolt1PPU	16.2	16.2	16.2	16.2
OVolt1PTime	100	100	100	100
OVolt3PPU	16.2	16.2	16.2	16.2
OVolt3PTime	100	100	100	100
<b>U/OV Loadshed Restore:</b>				
VoltRestoreEnable	Off	Off	Off	Off
VoltRestoreMode	Any Single Phase	Any Single Phase	Any Single Phase	Any Single Phase
VoltRestHiL	15.12	15.12	15.12	15.12
VoltRestLoL	13.68	13.68	13.68	13.68
Freq:62Schedule	300	300	300	300
Freq:62Abort	600	600	600	600
Freq:62Transient	0.3	0.3	0.3	0.3
VoltFreqRestSupEn	Off	Off	Off	Off

**Sensitive Earth Fault**

<b><u>Normal</u></b>	<b><u>Alternate 1</u></b>	<b><u>Alternate 2</u></b>	<b><u>Alternate 3</u></b>
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SEFBlock	Enable	Enable	Enable	Enable
SEFMinTrip	40	40	40	40
SEFTime	120	120	120	120
SEFReclnt	2	2	2	2
SEFNumOps	4	4	4	4

**Ungrounded SEF**

	<b><u>Normal</u></b>	<b><u>Alternate 1</u></b>	<b><u>Alternate 2</u></b>	<b><u>Alternate 3</u></b>
USEFBlock	Enable	Enable	Enable	Enable
USEFMinTrip	40	40	40	40
USEFTime	10	120	120	120
USEFV0	0.48	0.48	0.48	0.48
USEFNumOps	1	4	4	4
USEFOpInt#1	2	2	2	2
USEFOpInt#2	2	2	2	2
USEFOpInt#3	5	5	5	5
DirUSEFMTA	60	60	60	60
DirUSEF	NonDirectional	NonDirectional	NonDirectional	NonDirectional

**Directional Control**

	<b><u>Normal</u></b>	<b><u>Alternate 1</u></b>	<b><u>Alternate 2</u></b>	<b><u>Alternate 3</u></b>
DirMTA	60	60	60	60
DirPhs	NonDirectional	NonDirectional	NonDirectional	NonDirectional
DirGnd	NonDirectional	NonDirectional	NonDirectional	NonDirectional
DirNegSeq	NonDirectional	NonDirectional	NonDirectional	NonDirectional

**Low Set**

	<b><u>Normal</u></b>	<b><u>Alternate 1</u></b>	<b><u>Alternate 2</u></b>	<b><u>Alternate 3</u></b>
<b>Phase:</b>				
LSPEnable	Disable	Disable	Disable	Disable
LSPPU	3200	3200	3200	3200
LSPTIMEDelay	100	100	100	100
<b>Ground:</b>				
LSGEnable	Disable	Disable	Disable	Disable
LSGPU	1600	1600	1600	1600
LSGTimeDelay	100	100	100	100
<b>Negative Sequence:</b>				
LSQEnable	Disable	Disable	Disable	Disable
LSQPU	3200	3200	3200	3200
LSQTimeDelay	100	100	100	100

**Sync Check**

	<b><u>Normal</u></b>	<b><u>Alternate 1</u></b>	<b><u>Alternate 2</u></b>	<b><u>Alternate 3</u></b>
25DV:Enable	Disable	Disable	Disable	Disable
25DV:DLDB	Disable	Disable	Disable	Disable
25DV:DLHB	Disable	Disable	Disable	Disable

25DV:HLDB	Disable	Disable	Disable	Disable
25DV:HLHB	Disable	Disable	Disable	Disable
25DV:27	15840	15840	15840	15840
25DV:27DEAD	2640	2640	2640	2640
25DV:59	12240	12240	12240	12240
25DV:59LIVE	11520	11520	11520	11520
25DV	40	40	40	40
StaticAngleDelay	10	10	10	10
MechanismOpDelay	0.1	0.1	0.1	0.1